JH7100 SoC Boot User Guide

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About This Manual

Introduction

This document mainly describes the boot flow, the boot sources available for the JH7100 SoC and the Baremetal boot examples.

Prerequisite

In order to run the examples presented in this guide, the following are required:

- Ubuntu 18.04
- BeagleV[™] StarLight development board

Revision History

Version	Released	Change Description	
01	2021-6-7	The first official release.	

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1 Boot Sources

The GPIO is used to select the boot vector and BootLoader source and offer multiple methods to obtain the BootLoader image.

The JH7100 SoC can boot from one of the sources listed in the following table, as selected by the PAD_GPIO [62:60] values.

Processor	SCFG_boot_mode	PAD_GPIO [63]	Boot Vector	PAD_GPIO [62:60]
	0x1	-	SCFG_u74_reset_vector	-
U74	0x0 (default)	0x0	0x00_2000_0000, XIP Flash	 0x0: 1-bit quad SPI NOR flash memory 0x1: 4-bit quad SPI NOR flash memory 0x2: SDIO (Reserved)
		0x1	0x00_1840_0000, on- chip BootROM (32KB)	 0x3: eMMC (Reserved) 0x4: UART 0x5: USB (Reserved) 0x6: chiplink (Reserved) 0x7: SPI2AHB (Reserved)

Table 1-1 PAD	GPIO Value	es for Boot	Source	Selection
			000100	0010001011

Note:

1) The boot mode and boot source selection (PAD_GPIO [63]) can be read through syscon status registers.

- 2) Use the GPIO pad to select the vector and loader source by default.
- 3) PAD_GPIO [63] and PAD_GPIO [62:60] can be configured to 1 or 0 via pull-up/pull-down resister, button or jumper according to board hardware design.





2 Boot Flow

The boot process starts when the processor is released from reset, and jumps to the reset vector address (0x1840,0000 by default), located in the BootROM address space.

The boot flow is a multi-stage process. Each stage is responsible for loading the next stage. The typical boot flow is illustrated in the following figure.



Figure 2-1 Typical Boot Flow

2.1 BootROM

The BootROM is located in on-chip ROM, and the storage address is 0x1840,0000, which cannot be dynamically updated. After power-on, each HART jumps to 0x1800,0000 (located in RAM) by default and starts to execute BootROM.

The main function of the BootROM is to select the boot source and execute it. According to different hardware jumpers on the chip, only UART and QSPI sources are supported currently.

Source	Description
	Enter a simple command line. Load a limited size binary into the on-chip RAM and
UARI	execute it. This mode is mainly used for firmware update.
	Automatically load the 32K Bootloader to 0x1800,0000 (located in RAM) from
QSPI	address 0 of NOR Flash and jump to it.

Table 2-1 Boot Source Description

LIMITATION

The file loaded from NOR Flash cannot exceed 32KB.

2.2 BootLoader

The BootROM limits the size of data read from NOR Flash. The BootLoader reads DDRInit from 0x10000 in NOR Flash to 0x1808,0000 (located in RAM), and then jump to it for execution.

The DDRInit will initialize the DDR, then read fw_payload.bin (OpenSBI+Uboot, the file header contains file size information) from 0x40000 in NOR Flash to 0x8000,0000 (located in DDR), and then jump to it to execute the OpenSBI.

The normal output information is illustrated in the following figure.

✓ Serial-EVB ×	
bootloader version:210209-4547a8d ddr 0x00000000, 1M test ddr 0x00100000, 2M test DDR clk 2133M,Version: 210302-5aea32f 0 crc flash: 77d92512, crc ddr: 77d92512 crc check PASSED	
bootloader.	

Figure 2-2 BootLoader Output Example

2.3 OpenSBI

The binary of OpenSBI is packaged with the binary compiled by U-Boot in the way of payload to generate the final fw_payload.bin. The main functions of OpenSBI are:

- Provide basic system calls for Linux
- Switch the mode from M mode to S mode
- Jump to 0x8002,0000 (located in DDR) to execute U-Boot.

The normal output information is illustrated in the following figure.

Figure 2-3 OpenSBI Output Example

2.4 U-Boot

A

U-Boot runs at 0x8002,0000 and works in S mode. It contains basic file system and commonly used peripheral drivers (such as GMAC, UART, QSPI, USB, SDIO etc.). U-Boot can load the kernel image through ETH, UART, QSPI, SDIO or USB.

The following example describes how to load Linux 5.10 kernel image from SDIO.

The example assumes the installation of Ubuntu 18.04. Press Enter to confirm the operation or for the next command. 1. SD card partition.

wyh@xub-server:~/tmp/sft-riscvpi-freedom-u-sdk\$ wyh@xub-server:~/tmp/sft-riscvpi-freedom-u-sdk\$ wyh@xub-server:~/tmp/sft-riscvpi-freedom-u-sdk\$ sudo fdisk /dev/sdb	Use the fdisk command to partition the SD card.
Welcome to fdisk (util-linux 2.31.1). Changes will remain in memory only, until you decide to write them. Be careful before using the write command.	
Command (m for help): d Selected partition 1 Partition 1 has been deleted. Command (m for help): d No partition 1s defaned yet! .could.not.delete.partition.94918161587593	Repeat command "d"to delete all original partitions.
Command (m for help): n Partition number (1-128, default 1): First sector (34-31116254, default 2048): Last sector, +sectors or +size{K,M,G,T,P} (2048-31116254, default 31116254): +256M Created a new partition 1 of type 'Linux filesystem' and of size 256 MiB.	Use command "n" to create the first partition with a size of 256M.
Command (m for help): n Partition number (2-128, default 2): F First sector (526336-31116254, default 526336): L Last sector, +sectors or +size{K,M,G,T,P} (526336-31116254, default 31116254): C Created a new partition 2 of type 'Linux filesystem' and of size 14.6 GiB.	Use command "n" to create a second partition, the size of which is the remaining space of the SD card.
Command (m for help): w The partition table has been altered. Calling ioctl() to re-read partition table. Syncing disks.	Use command "w" to write the partition information into the partition table.

Figure 2-4 SD Card Partition Command Explanation

2. Format the partition.



Figure 2-5 Format the Partition

- 3. Generate image.fit from Freelight U SDK, please refer to the detailed guidelines in the link <u>https://github.com/starfive-tech/freelight-u-sdk</u>.
- 4. Copy the boot file.



Figure 2-6 Copying the Boot File

5. Load the kernel (Linux 5.10 as an example).

The addresses 0x80200000, 0x86100000 and 0x86000000 have been specified when compiling and generating image.fit and cannot be modified.

 Image: fit image: fit

starfie flootm loados s{kernel_addr_r} Load the kernel to the corresponding address in the D	DR.
StarFive # Trying to execute a command out of order bootm - boot application image from memory	
<pre>Usage: loot application image stored in memory - boot application image stored in memory passing arguments arg' when booting a Linux kernel, when booting a Linux kernel which requires a flat device-tree a third argument is required which is the address of the device-tree blob. To boot that kernel without an intrd image, use a '-' for the second argument. If you do not pass a third a bd_info struct will be passed instead</pre>	
For the new multi component utmage format (FIT) addresses must be extended to include component or configuration unit name: addr:subimg_uname> - direct component image specification addr#cconf_uname> - configuration specification Use iminfo command to get the list of existing component images and configurations.	
<pre>sub-commands to do part of the bootm sequence. The sub-commands must be issued in the order below (it's ok to not issue all sub-commands): start [addr [arg]] loados - load Os image refusk - relocate flat device tree cmd1ine - OS specific command line processing/setup bdt - OS specific bd_info processing prep - OS specific bd_info processing start [ov flatted Device Tree blob at 0x86000000 [filesize] 0x86000000 Booting using the fdt blob at 0x86000000 end 00000008600905f</pre>	
starting kernel	
0.000000] tinux version 5.10.6-ga4e958526177-dirty (clivia@ubuntu) (riscv64-buildroot-linux-gnu-gcc.br_real (03:38:60 PDT 2021 non-on-on-on-on-on-on-on-on-on-on-on-on-	
0.0000000 DM32 [mem 0x000000080200000-0x000000fffffff] 0.000000 DM32 [mem 0x00000008020000-0x000000fffffff] 0.000000 Movable zone start for each node	
0.0000001 rode 0: [mem 0x000000080200000-0x000000068ffffff] 0.0000001 rode 0: [mem 0x000000006800000-0x000000068ffffff] 0.0000001 rode 0: [mem 0x0000000000000000000000000000000000	
L 0.000000 DM32 zone: 716i pages: 2084352 0.000000 DM32 zone: 716i pages used for memmap	
0.0000000 DMA32 zone: 0 pages reserved 0.0000000 DMA32 zone: 511488 pages, LIF0.batch:63	
0.0000000 Normal zone: 21504 pages used for memmap 0.000000 Normal zone: 1572864 pages, LIFO batch:63	
0.000000 software IO TLB: mapped [mem 0x0000000cc000000-0x0000000000] (64MB) 0.000000 SBI specification v0.2 detected 0.000000 SBI specification v0.2 detected 0.000000 SBI specification v0.2 detected	

Figure 2-7 Loading the Kernel