

EFSL

Embedded Filesystems Library - 0.3

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1 Document Outdated!

This document is outdated and is in the progress of being renewed.

If you are just starting with Efsl, we recommend you to start with the stable 0.2-branch. This version is currently not really usable, and is intended for people working on the code.

2 Preface

2.1 Project aims

The EFSL project aims to create a library for filesystems, to be used on various embedded systems. Currently we support the Microsoft FAT filesystem family. It is our intention to create pure ANSI C code that compiles on anything that bears the name 'C compiler'. We don't make assumptions about endianness or how the memory alignment is arranged on your architecture.

Adding code for your specific hardware is straightforward, just add code that fetches or writes a 512 byte sector, and the library will do the rest. Existing code can be used, writing your own code is only required when you have hardware for which no target exists.

2.2 Project status

Efsl currently supports FAT12, FAT16 and FAT32. Read and write has been tested and is stable. Efsl runs on PC (GNU/Linux, development environment), TMS C6000 DSP's from Texas instruments, and ATMega's from Atmel. You can use this code with as little as 1 kilobyte RAM, however if you have more at your disposal, an infinite amount can be used as cache memory. The more memory you commit, the better the performance will be.

2.3 License

This project is released under the Lesser General Public license, which means that you may use the library and it's sourcecode for any purpose you want, that you may link with it and use it commercially, but that ANY change to the code must be released under the same license. We would appreciate if you would send us a patch when you add support for new hardware, but this is not obligatory, since it falls under linking as far as the LGPL is concerned.

3 Getting started

3.1 On Linux (file) (0.2)

Debugging efsl on embedded devices is a rather hard job, because you can't just printf debug strings or watch memory maps easily. Because of that, core development has been performed under the Linux operating system. Under Linux, efsl can be compiled as library and used as a userspace filesystem handler. On Unix- style operating system (like Linux), all devices (usb stick, disc, ...) can be seen as a file, and as such been opened by efsl.

In the following section, we will explain how to get started using efsl as userspace filesystem handler. However, please note that the main focus for efsl is to support embedded systems, which usually don't even have 1% of the memory you have on a PC. Accessing files on a FAT-filesystem with efsl will be much slower than when accessing these files with the Linux FAT kernel modules.

3.1.1 Download & Compile

Let's get started:

1. Get the latest release of efsl on <http://www.sf.net/projects/efsl/> and put it in your homedir
2. Unpack the library (tar xvfj efsl-version.tar.bz2)
3. Get inside the directory (cd ~/efsl)
4. Create a symlink from `Makefile-LINUX` to `Makefile` (ln -s Makefile-LINUX Makefile)
5. Copy `conf/config-sample-linux.h` to `conf/config.h` (cp conf/config-sample-linux.h conf/config.h)
6. Compile the library (make lib)
7. Find the compiled filesystem library (libefsl.a) in the current directory

If you got any errors with the steps above, please check that that you have the following packages installed: tar, gcc, libgcc, binutils & make.

3.1.2 Example

Since efsl itself is only a library, it's not supposed to do anything out of the box, than just compile. To get started, we'll show here a small example program that opens a file on a disc/usb-stick/floppy that contains a FAT-filesystem and prints it's content to stdout.

First, create a new directory in which you put the compiled efsl-library (`libefsl.a`) and create a new file called `linuxtest.c` containing:

```

1  #include <stdio.h>
2  #include <efs.h>
3
4  int main(void)
5  {
6      EmbeddedFileSystem efs;
7      EmbeddedFile file;
8      unsigned short i,e;
9      char buf[512];
10
11     if(efs_init(&efs,"/dev/sda")!=0){
12         printf("Could_not_open_filesystem.\n");
13         return(-1);
14     }
15
16     if(file_fopen(&file,&efs.myFs,"group",'r')!=0){
17         printf("Could_not_open_file.\n");
18         return(-2);
19     }
20
21     while(e=file_read(&file,512,buf)){
22         for(i=0;i<e;i++)
23             printf("%c",buf[i]);
24     }
25
26     return(0);
27 }

```

Some extra information on the code above:

- Line 1-2: The header files for stdio (used for printf) and efs are included. When using the basic efs functions, `efs.h` is the only header file of the efs library that needs to be included.
- Line 6: The object `efs` is created, this object will contain information about the hardware layer, the partition table and the disc.
- Line 7: The object `file` is created, this object will contain information about the file that we will open on the `efs`-object.
- Line 9: A buffer of 512 bytes is allocated. This buffer will be filled by `fread` with data.
- Line 11-14: Call of `efs_init`, which will initialize the `efs`-object. To this function we pass:
 1. A pointer to the `efs`-object.
 2. A pointer to the file that contains the partition table / file system (in this example, we select a device as file).

If this function returns 0, it means that a valid fat partition is found on the device given. If no valid fat-filesystem is found, or the file does not exist, the function returns a negative value. In this example we then print an error message and quit.

- Line 16-19: Call of `file_fopen()` , which will initialize the file-object. To this function we pass:
 1. A pointer to the file-object.
 2. A pointer to the filesystem-object.
 3. A pointer to the filename.
 4. A char containing the the mode (read, write, append).

If this function returns 0, it means the file has successfully been opened for reading / writing / appending. If the file could not be opened, a negative value is returned.

- Line 21-24: Call of `file_read()` , which will read a given value of bytes (in this example 512) from a file and put it's content into the buffer passed (in this example called buf). This function returns the amount of bytes read, so the while-loop will be executed as long as there are bytes left in the file. The code inside the while-loop will print all characters in the buffer.

3.1.3 Testing

So now let's test the program:

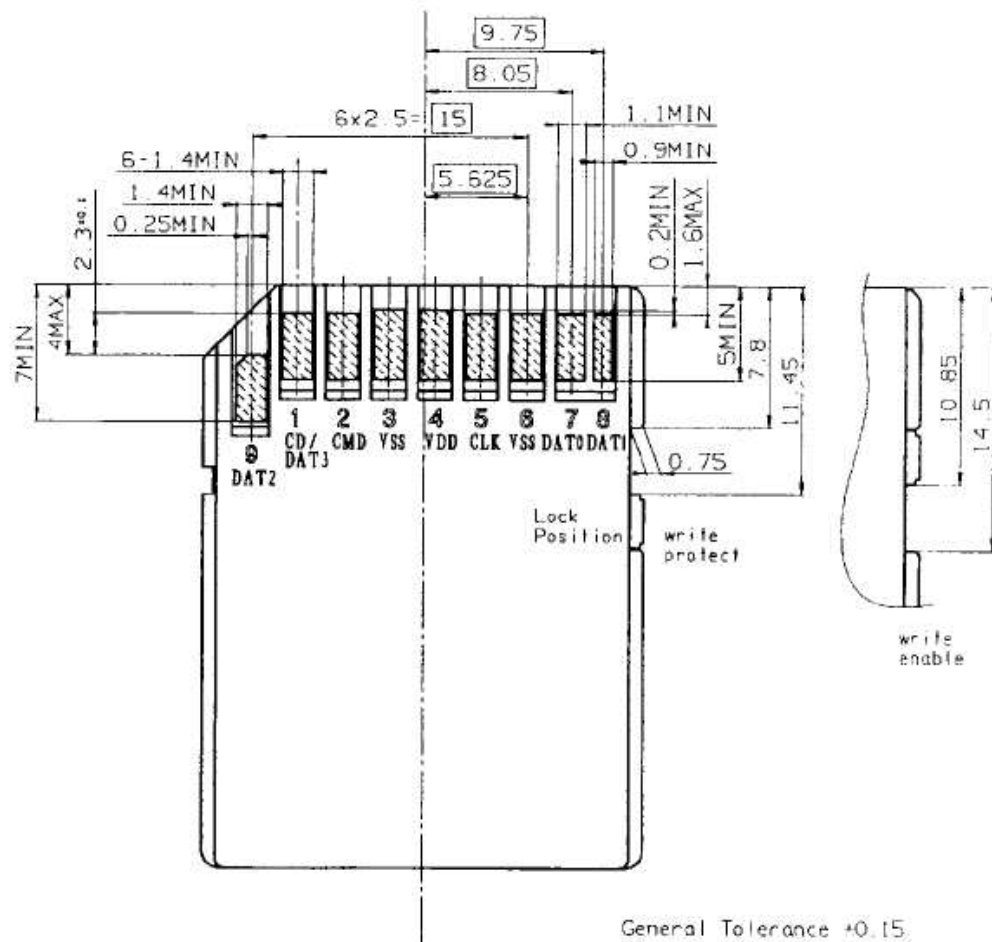
1. Compile the program (`gcc -I/home/user/efsl/inc/ -I/home/user/efsl/conf -o linuxtest linuxtest.c -L./ -lefs`).
2. Insert a usb-disc, floppy, mp3-stick, ... with a valid fat-filesystem on it.
3. Mount the device, copy the file `/etc/group` on it's root dir & umount it.
4. Check that you have permission to access the device (`chown username /dev/sda*`)
5. Run the program (`./linuxtest`)

3.2 On AVR (SD-Card) (0.3)

This section describes how to implement Efs1 on a AVR μC connected to an SD-Card (SPI). For getting efs1 to compile, the avr-gcc compiler and avr-libc library are required. On Windows you should install WinAVR (<http://winavr.sourceforge.net/>), on Linux you can install the packages separately (see http://www.nongnu.org/avr-libc/user-manual/install_tools.html for a nice howto).

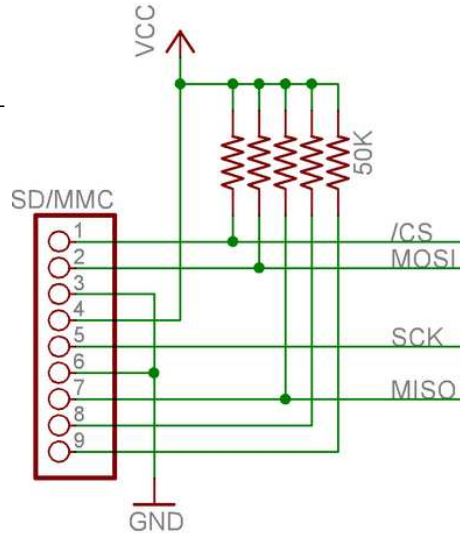
3.2.1 Hardware

First, you need set up a prototype in which you connect the CD, CMD, DAT0 & CLK lines from the SD-Card to /CS, MOSI, MISO & SCK from the Atmega.



Connect the following lines on the SD-card:

- Pin 9 (DAT2) - NC
(or pull-up to 3.3V)
- Pin 1 (CD) - Any pin on the Atmega128
- Pin 2 (CMD) - MOSI
(pin 12 on the Atmega128)
- Pin 3 (Vss) - GND
- Pin 4 (Vdd) - +3.3V
- Pin 5 (CLK) - SCK
(pin 11 on the Atmega128)
- Pin 6 (Vss) - GND
- Pin 7 (DAT0) - MISO
(pin 12 on the Atmega128)
- Pin 8 (DAT1) - NC
(or pull-up to 3.3V)



Remark: this schematic includes pull-up's to 3.3V, which can be left off.

Remark 1: Make sure that your μC is running on 3.3V, so you don't damage your SD-Card.

Remark 2: CD is currently static set to PB0, but will become variable in future releases.

3.2.2 Download & Compile

Let's get started:

1. Get the latest release of efsl on <http://www.sf.net/projects/efsl/>
2. Unpack the library (on Windows, you can use WinACE or WinRAR)
3. Copy in directory `conf` the file `config-avr.h` to `config.h`
4. Copy in directory `conf` the file `config-avr.makefile` to `config.makefile`
5. Compile the library (`make avr`)

Now you should have the following files in a directory called lib:

- libefsl-base.a
- libefsl-fs-vfat.a
- libefsl-prot-sdspi.a
- libefsl-hwd-atmega_spi.a

3.2.3 Example

Since Efsl itself is only a library, it's not supposed to do anything out of the box, than just compile. To get started, we'll show here a small example program that opens an existing file and writes the content to a new file.

First, create a new directory in which you put the compiled efsl-library (libefsl.a) and create a new file called `avrtest.c` containing:

```
1  #include <efs.h>
2  #include <sd.h>
3  #include <atmega_spi.h>
4
5  void hang(void);
6
7  void main(void)
8  {
9      efsl_storage_conf storage_conf;
10     efsl_fs_conf fs_conf;
11
12     efsl_storage storage;
13     efsl_fs fs;
14     File file_r;
15     File file_w;
16
17     atmegaSpiInterface spi_interface;
18     SdSpiProtocol sd_protocol;
19
20     char buf[512];
21     unsigned short e;
22
23     /* Init */
24     spi_interface.pinSelect=0x01;
25
26     sd_protocol.spiHwInterface=&spi_interface;
27     sd_protocol.spiHwInit=(void *) atmega_spi_init;
28     sd_protocol.spiSendByte=(void *) atmega_spi_send;
29
30     storage_conf.hwObject=&sd_protocol;
31     storage_conf.if_init_fptr=(void *) sd_Init;
32     storage_conf.if_read_fptr=(void *) sd_readSector;
33     storage_conf.if_write_fptr=(void *) sd_writeSector;
34     storage_conf.if_ioctl_fptr=(void *) sd_ioctl;
35     storage_conf.ioman_bufmem=0;
36
37     fs_conf.no_partitions=0;
38     fs_conf.storage=&storage;
39
40     if (efsl_initStorage(&storage,&storage_conf)){
41         hang();
```

```

42     }
43
44     if (efsl_initFs(&fs,&fs_conf)){
45         hang();
46     }
47
48     if (file_fopen(&file_r,&fs.filesystem,"orig.txt",'r')!=0){
49         hang();
50     }
51
52     if (file_fopen(&file_w,&fs.filesystem,"copy.txt",'w')!=0){
53         hang();
54     }
55
56     if (file_fopen(&file_r,&efs.myFs,"orig.txt",'r')!=0){
57         hang();
58     }
59
60     while((e=file_read(&file_r,512,buf))){
61         file_write(&file_w,e,buf);
62     }
63
64     file_fclose(&file_r);
65     file_fclose(&file_w);
66
67     fs_umount(&fs.filesystem);
68
69     hang();
70 }
71
72 void hang(void)
73 {
74     while((1))
75         NOP();
76 }

```

Some extra information on the code above: TODO

3.2.4 Testing

So now let's test the program:

1. Compile the program:

- On Linux (with avr-gcc): `avr-gcc -I/home/user/src/base/include -I/home/user/src/include -I/home/user/src/fs/vfat/include -I/home/user/src/hwdrivers/atmega.s -I/home/user/src/protocols/sdcard_spi/include -I/home/user/conf -ffreestanding -mmcu=atmega128 -Os -o avrtest.o avrtest.c -L/home/user/lib -lefsl-base -lefsl-fs-vfat -lefsl-hwd-atmega_spi -lefsl-prot-sdspi`

- On Windows (with WinAVR): replace all slashes with backslashes
2. Generate a hexfile (`avr-objcopy -j .text -j .data -O ihex avrtest.o avrtest.hex`)
 3. Connect an SD-card to your Atmega128 with a file called `orig.txt` on it.
 4. Flash the hex file into your μC .
 - On Linux: `avrdude -P /dev/ttyUSB0 -c stk500 -p m128 -Uflash:w:avrtest.hex`
 - On Windows: use Atmel AVR-Studio
 5. Reset your μC and wait some time (depending on how big the file `orig.txt` is).
 6. Disconnect the SD-card, so you can put it in your card reader and find out if the file `orig.txt` is copied to `copy.txt` .

3.3 On DSP (SD-Card) (0.2)

This section will tell you everything you need to know to start using the embedded filesystems library on a TMS Digital Signal Processor from Texas Instruments. The only thing that is required is that you have a McBSP port available, and that your DSP support CLOCKSTOP mode, which is required to connect a SPI compatible device.

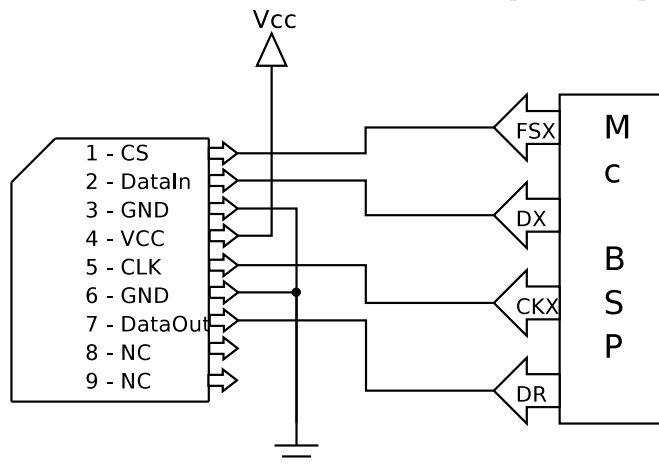
There are special DSP's from TI which have a special MMC/SD card controller, if you want to use this special interface you will have to create a hardware endpoint for it. This section only describes connecting an SD card to a normal McBSP port, since every TI DSP has at least one of them.

3.3.1 Hardware

Connecting the SD card to the McBSP is straightforward, you will have to make 4 data related connections, Vcc and ground, resulting in a 6 wire interface.

SD Card Interface			McBSP Interface	
1	CS	Chip select	FSX	Frame Sync Transmit
2	MOSI	Master out Slave In	DX	Data transmit
3	GND	Supply Ground		
4	Vcc	Supply voltage (3.3 Volt)		
5	Clk	Clock	CLKX	Clock Transmit
6	GND	Supply ground		
7	MISO	Master in Slave out	DR	Data receive
8	NC	Not connected		
9	NC	Not connected		

You can optionally pull the DataIn and DataOut lines up to Vcc with a 10k Ω resistor, but we found that this was not required for operation.



The frame sync from the McBSP port is used to select the card whenever a databyte has to be transferred, it is connected to the chip select of the SD card. The DX and DR pins are connected to the SDcard's DataIn and DataOut lines respectively. Finally the McBSP will have to generate a clock for the SDcard so that it can perform operations, this is accomplished by connecting the clock transmit line of the McBSP port to the CLK pin of the SDCard.

3.3.2 McBSP configuration

McBSP Register Explanations			
SPCR			Serial Port Control Register
Name	Bit	Value	Value (0x00001800 0x00410001)
RRST	0	1b	The serial port receiver is enabled
XRST	16	1b	The serial port transmitter is enabled
CLKSTP	12:11	11b	Clock starts on falling edge without delay(see CLKXM)
GRST	22	1b	Sample rate generator is pulled out of reset
PCR			Pin Control Register
Name	Bit	Value	Value 0x00000A0C
CLKXP	1	0b	Transmit data on the rising edge of the clock
FSXP	3	1b	Frame Sync (Chip select on SD card) is active low
CLKXM	9	1b	McBSP is a master in SPI mode and generates the clock based on the sample rate generator
FSXM	10	1b	Frame sync is determined by the sample rate generator
RCR/XCR			Receive/Transmit Control Register
Name	Bit	Value	Value 0x00010000
RWDLEN	7:5	000b	Receive element is 8 bits (1byte) large
XDATDLY	17:16	01b	1 bit data delay (after frame sync)
SRGR			Sample Rate Generator
Name	Bit	Value	Value 0x20000002
CLKSM	29	1b	The sample rate generator clock is derived from the internal clock
FSGM	28	0b	The transmit frame sync signal is generated on every DXR to XSR copy
CLKGDV	7:0	0x02h	The clock divider

4 Configuring EFSL (0.2)

In this section we're going to talk about the configuration file (`config.h`), that defines the behavior of the library. In the configuration files there are many settings, most of which default to safe or 'standard' compliant settings.

For every platform we try to deliver a sample configuration, with setting tweaked for that architecture. This documentation only refers to the general elements which are tied to the library rather than the target hardware.

4.1 Hardware target

Here you will define what kind of hardware you will be using. Please refer to section 6.3 to learn how to write a hardware endpoint. Here you must **#define** the name of your hardware endpoint. The following list contains the endpoints that the library ships with.

<code>HW_ENDPOINT_LINUX</code>	This endpoint uses a regular file as a "disc" containing a filesystem. This is a great endpoint for testing and debugging. All development is done using this emulation.
<code>HW_ENDPOINT_ATMEGA128_SD</code>	This endpoint is for the Atmel ATmega 128 with an SD card attached to the SPI pins of the device. Several settings that are specific for this endpoint can be found in the AVR sample configuration. A Makefile is also provided for compiling the EFSL library using <code>avr-gcc</code> .
<code>HW_ENDPOINT_DSP_TI6713_SD</code>	This endpoint is for a TI DSP, it should work with any McBSP port, due to the infinite amount of options, you should refer to the source code of this endpoint for fine tuning, or selecting what port to use (defaults to McBSP0).

4.2 Memory configuration

This section only has one option, called `BYTE_ALIGNMENT` . If you define this keyword the library will assume that your CPU is capable of accessing the memory in any way it sees fit. This is the case on AVR, because they are 8 bit processors, and it is also the case on Intel x86 hardware. Both architectures can read and write words, or double words on any location in memory, be it word aligned or not.

However, some CPU's, are not capable of doing this, and require that all double words are aligned on a double word boundary, and all word are aligned on a word boundary. This causes problems with some of the casts that are performed in EFSL. If you have such a CPU, then you must comment this option out. The effect is that special functions will be used to copy or cast memory. These functions work around the problem by using `memCpy`, or manually copying elements of the structs that are normally cast when `BYTE_ALIGNMENT` is defined.

If you have an 8 bit architecture, or are running on PC, there is no need to turn this off. If you do, the library will work fine, and maybe even without slowdown.

On architectures that do have the alignment problem, you should turn this flag off. Failure to do so will result in undefined behavior.

4.3 Cache configuration

This section is dedicated to configuring the cache memory for the library. Caching is performed by the IOMan object, see section 6.4.

IOMAN_NUMBUFFER

This number determines how much memory will be used for caching. Since this is sector based one `IOMAN_NUMBUFFER` equals to 512 bytes of memory, plus a small overhead in settings (approximately 8 bytes). This number is also affected by `IOMAN_NUMITERATIONS`.

You should carefully consider how much memory you will dedicate to caching. A too low number will cause excessive data transfer to and from the disc, where a too high number will simply be a waste of memory.

A good rule of thumb is to use 1 buffer per filesystem you create, and 2 buffers per file you want to use simultaneously. So for a simple application with one filesystem, and one file operation, 2 or 3 buffers will be fine. If you have memory to spare, you can use 6 buffers. Using more buffers will have a minimal effect on performance.

If you want to seek and rewrite portions of a file, add an extra buffer for that file. Using the list function or creating directories will be disc intensive, try to smoothen it by using an extra 3 buffer for either operation.

It is perfectly possible to have multiple files open for reading and writing, on different filesystems, with listing etc and only using 1 buffer. It will be a tough blow on performance though.

IOMAN_NUMITERATION

This number controls how many stack places each cache place gets. Refer to the IOMan section for an explanation. In short, if you only have 1 buffer, leave it at 3. If you use more than 4 buffers try decreasing the number to 2 or 1 for a small memory gain.

If you get errors, it means you have set it too low (see error support). It is best to leave this at the default setting (do not increase it), unless you know what you are doing.

IOMAN_DOMEMALLOC

This configures how IOMan will get its memory. If you leave it enable, the memory will be allocated by IOMan itself. That means that when you declare the IOMan object it will have a member the size of $512 \cdot \text{IOMAN_NUMBUFFER}$. That also means that that huge lump of memory will reside on the stack. On

a true embedded platform with no malloc, this is your best option. The last argument of `ioman_init` will be ignored.

If you comment this out, IOMan will take a `eu_int8*` pointer as it's third argument to `ioman_init`. It will use the memory pointed to as cache. You will have to make sure it's reserved and of the correct size. This allows you to put the memory on the heap, or perform special tricks like deallocating it without having to umount your filesystem and open files. On systems with malloc, this is the recommended setting.

If you use the efs wrapper object, please look at the `efs_init` documentation on how to pass the ioman pointer.

4.4 Pre-allocation

Our VFAT module supports the concept of pre-allocation. When writing files, for example log files, it is usually done with tiny bits a time. That is not the most efficient way, but it is usually the only solution that works on embedded systems. Every time you cross a cluster boundary with your write, the library has to search a new cluster (reading the FAT), allocate it (write to the FAT).

Clearly, this is a waste. The solution we came up with was preallocating. This means that when you write to a file, and `fwrite` sees that it needs to allocate more clusters, it will allocate too many of them. Since this is done in one operation, it requires usually only one read and one write to the FAT. This can save up to 50% disc I/O in some applications.

The drawback is that the allocation happens in larger chunks, if you do this with many files, you might end up with larger than normal amounts of slackspace.

We have also implemented this feature for directories. This is very useful if you have to create a lot of small files, since the directories grow by larger portions then.

CLUSTER_PREALLOC_FILE

This number determines the default value of extra clusters that will be allocated with every sizeincrease. For example, if `fwrite` calculates that it needs 7 clusters, and `CLUSTER_PREALLOC_FILE` is 30 then `efsl` will allocate 37 clusters. This means (assuming every write needs 7 clusters) that the next 4 writes won't require any write operation to the FAT (and due to the cluster cache the FAT will probably have to be read only once).

The value you put here will be the default value, it can be changed per file object. (not yet implemented).

CLUSTER_PREALLOC_DIRECTORY

The same explanation as above counts, only this value is used for directories. Generally you should not put this above 10 (unless your speed tests prove otherwise off course).

4.5 Endianness

The Microsoft FAT filesystem was originally created to be run on Intel compatible hardware. Therefore the Microsoft programmers decided to record all data on the disc in little endian format. Our library supports running on big endian devices. Here you can select whether your target CPU is little or big endian.

Running on big endian will cause some performance lose because (rather simple) calculations have to be made to all numbers that have to interpreted by the library. This does not apply to data within the files off course.

If the flag `#LITTLE_ENDIAN` is set, `efsl` will assume that your hardware is little endian. If you have a big endian system, you should comment this out. The function `fs_checkEndian` will tell you if you have selected the right endianness, this is a check you might want to use.

4.6 Date and time

This flag determines if you want to have date and time support. With date and time support we mean that when you create or update a file the directory entry will receive the correct date and time stamp.

Please refer to section 5.1 to learn more about how this works.

If you disable date and time support by commenting the `#DATE.TIME.SUPPORT` then all dates and times that need to be created or updated will be set to zero, which in FAT land corresponds to the first of January of the year 1970.

4.7 Errors

When the library encounters an error, there be an error cascade moving from the error-causing object to the topmost object where the request started. Seen from userland this gives you extremely little information, usually nothing more than fail or success.

Every object in the library has an optional error field, that contains a unique number that corresponds to a specific error. If you examine every error field you can see exactly where the error was started and what the effect was on the higher level objects.

In a more practical sense you can display an error number or explanation to your users, giving yourself or them a better chance to correct or avoid the problem. Please see the section on error on what every value means.

4.8 Debug

This will turn debug support on or off. When enable (and your platform has a means of output that is supported by `EFSL`) it you will see messages you have created yourself, or that are printed by the library. By default the library is very silent, only very critical errors might get printed out.

This option is depreciated and is left in for backward compatibility.

5 EFSL Functions

5.1 Date and time support (0.2)

The EFSL library supports setting and updating all date and time fields supported by the filesystem. In order to do this the library must know the current time and date at all times. Since it has to run everywhere, there is no standard mechanism to get the date/time, and some systems do not have a clock.

With default configuration there is no date or time support, you have to turn it on manually in the configuration file `config.h`. You will have to uncomment the field named `#define DATE_TIME_SUPPORT`, in order to activate date/time support.

Furthermore you will have to provide the library with date and time information. A set of defines was used for this, when date/time support is not enabled, the defines automatically return `0x0000` for all time and date fields, so there is no performance suffer when you do not need date/time support. If you do need it you will have to provide 6 functions to the library that will tell it the time. Since these functions may get called often, it is highly recommended that you cache the time result somewhere so you can serve the library directly from ram. If you do not do this and your RTC request take a lot of time, you may suffer large losses in read or write operations depending on your hardware.

The six functions are:

- `euint16 efsl_getYear(void)`
- `euint8 efsl_getMonth(void)`
- `euint8 efsl_getDay(void)`
- `euint8 efsl_getHour(void)`
- `euint8 efsl_getMinute(void)`
- `euint8 efsl_getSecond(void)`

Internally the library will recalculate these numbers to match the filesystem that is currently in use.

5.2 efs_init (0.2)

Purpose

Initializes the hardware and the software layer.

Prototype

```
esint8 efs_init(EmbeddedFileSystem *efs, eint8* opts);
```

Arguments

Objects passed to `efs_init` :

- `efs` : empty EmbeddedFileSystem object
- `opts` : character string containing options, depending on what interface you are using:
 - Linux: `opts` points to the path to the device
 - AVR: `opts` points to the card enable pin (TODO)
 - DSP: `opts` points to the card enable memory address (TODO)

Return value

Returns 0 if no errors are detected.

Returns non-zero if a low-level error is detected:

- Returns -1 if the interface could not be initialized.
- Returns -2 if the filesystem could not be initialized.

Example

```
1  #include "efs.h"
2
3  void main(void)
4  {
5      EmbeddedFileSystem efs1;
6      esint8 ret;
7
8      DBG((TXT(" Will_init_efs1_now.\n" )));
9      ret=efs_init(&efs1,"/dev/sda");
10     if (ret==0)
11         DBG((TXT(" Filesystem_correctly_initialized.\n" )));
12     else
13         DBG((TXT(" Could_not_initialize_filesystem_(err_\\%d).\n" ),ret));
14 }
```

5.3 file_fopen (0.2)

Purpose

Searches for file and initializes the file object.

Prototype

```
esint8 file_fopen(File *file, FileSystem *fs, eint8 *filename, eint8
mode);
```

Arguments

Objects passed to `file_fopen` :

- `file` : pointer to a File object
- `fs` : pointer to the FileSystem object
- `filename` : pointer to the path + filename
- `mode` : mode of opening, this can be:
 - `'r'`: open file for reading
 - `'w'`: open file for writing
 - `'a'`: open file for appending

Return value

Returns 0 if no errors are detected.

Returns non-zero if an error is detected:

- Returns -1 if the file you are trying to open for reading could not be found.
- Returns -2 if the file you are trying to open for writing already exists.
- Returns -3 if no free spot could be found for writing or appending.
- Returns -4 if mode is not correct (if it is not `'r'`, `'w'` or `'a'`).

Example

```
1    #include "efs.h"
2
3    void main(void)
4    {
5        EmbeddedFileSystem efs1;
6        File file_read , file_write;
7
```

```

8      /* Initialize efsl */
9      DBG((TXT(" Will_init_efs_lnow.\n" )));
10     if (efs_init(&efs_l, "/dev/sda")!=0){
11         DBG((TXT(" Could_not_initialize_filesystem_(err_\\%d).\n" ), ret));
12         exit(-1);
13     }
14     DBG((TXT(" Filesystem_correctly_initialized.\n" )));
15
16     /* Open file for reading */
17     if (file_fopen(&file_read, &efs_l.myFs, "read.txt", 'r')!=0){
18         DBG((TXT(" Could_not_open_file_for_reading.\n" )));
19         exit(-1);
20     }
21     DBG((TXT(" File_opened_for_reading.\n" )));
22
23     /* Open file for writing */
24     if (file_fopen(&file_write, &efs_l.myFs, "write.txt", 'w')!=0){
25         DBG((TXT(" Could_not_open_file_for_writing.\n" )));
26         exit(-2);
27     }
28     DBG((TXT(" File_opened_for_writing.\n" )));
29
30     /* Close files & filesystem */
31     fclose(&file_read);
32     fclose(&file_write);
33     fs_umount(&efs_l.myFs);
34 }

```

5.4 file_fclose (0.2)

Purpose

Updates file records and closes file object.

Prototype

```
esint8 file_fclose(File *file);
```

Arguments

Objects passed to `file_fopen` :

- `file` : pointer to a File object

Return value

Returns 0 if no errors are detected.

Returns non-zero if an error is detected.

Example

```
1    #include "efs.h"
2
3    void main(void)
4    {
5        EmbeddedFileSystem efs1;
6        File file;
7
8        /* Initialize efs1 */
9        DBG((TXT(" Will_init_efs1_now.\n")));
10       if(efs_init(&efs1, "/dev/sda")!=0){
11           DBG((TXT(" Could_not_initialize_filesystem_(err_%d).\n"), ret));
12           exit(-1);
13       }
14       DBG((TXT(" Filesystem_correctly_initialized.\n")));
15
16       /* Open file for reading */
17       if(file_fopen(&file, &efs1.myFs, "read.txt", 'r')!=0){
18           DBG((TXT(" Could_not_open_file_for_reading.\n")));
19           exit(-1);
20       }
21       DBG((TXT(" File_opened_for_reading.\n")));
22
23       /* Close file & filesystem */
24       fclose(&file);
```



```
25         fs_umount(&efs.myFs);  
26     }
```

5.5 file_read (0.2)

Purpose

Reads a file and puts it's content in a buffer.

Prototype

```
euint32 file_read (File *file, euint32 size, euint8 *buf);
```

Arguments

Objects passed to `file_read` :

- `file` : pointer to a File object
- `size` : amount of bytes you want to read / put in buf
- `buf` : pointer to the buffer you want to store the data

Return value

Returns the amount of bytes read.

Example

```
1    #include "efs.h"
2
3    void main(void)
4    {
5        EmbeddedFileSystem efs1;
6        euint8 buffer[512];
7        euint16 e, f;
8        File file;
9
10       /* Initialize efs1 */
11       DBG((TXT(" Will_init_efs1_now.\n" )));
12       if(efs_init(&efs1,"/dev/sda")!=0){
13           DBG((TXT(" Could_not_initialize_filesystem_(err_\\%d)\\.\\n"),ret));
14           exit(-1);
15       }
16       DBG((TXT(" Filesystem_correctly_initialized.\\n" )));
17
18       /* Open file for reading */
19       if(file_fopen(&file, &efs1.myFs, "read.txt", 'r')!=0){
20           DBG((TXT(" Could_not_open_file_for_reading.\\n" )));
21           exit(-1);
22       }
23       DBG((TXT(" File_opened_for_reading.\\n" )));
```

```

24
25      /* Read file and print content */
26      while((e=file_read(&file,512,buffer)){
27          for(f=0;f<e;f++)
28              DBG((TXT("\%c"),buffer[f]));
29      }
30
31      /* Close file & filesystem */
32      fclose(&file);
33      fs_umount(&efs.myFs);
34  }

```

5.6 file_write (0.2)

Purpose

Reads a file and puts it's content in a buffer.

Prototype

```
euint32 file_write(File *file, euint32 size, euint8 *buf)
```

Arguments

Objects passed to `file_read` :

- `file` : pointer to a File object
- `size` : amount of bytes you want to write
- `buf` : pointer to the buffer you want to write the data from

Return value

Returns the amount of bytes written.

Example

```
1  #include <string.h>
2  #include "efs.h"
3
4  void main(void)
5  {
6      EmbeddedFileSystem efs1;
7      euint8 *buffer = "This_is_a_test.\n";
8      euint16 e=0;
9      File file;
10
11     /* Initialize efs1 */
12     DBG((TXT(" Will_init_efs1_now.\n")));
13     if(efs_init(&efs1,"/dev/sda")!=0){
14         DBG((TXT(" Could_not_initialize_filesystem_(err_%d).\n"),ret));
15         exit(-1);
16     }
17     DBG((TXT(" Filesystem_correctly_initialized.\n")));
18
19     /* Open file for writing */
20     if(file_fopen(&file, &efs1.myFs, "write.txt", 'w')!=0){
21         DBG((TXT(" Could_not_open_file_for_writing.\n")));
22         exit(-1);
23     }
```

```

24         DBG((TXT(" File _opened_for _reading.\n")));
25
26         /* Write buffer to file */
27         if( file_write(&file , strlen(buffer), buffer) == strlen(buffer) )
28             DBG((TXT(" File _written.\n")));
29         else
30             DBG((TXT(" Could _not _write _file.\n")));
31
32         /* Close file & filesystem */
33         fclose(&file);
34         fs_umount(&efs.myFs);
35     }

```

5.7 mkdir (0.2)

Purpose

Creates a new directory.

Prototype

```
esint8 mkdir(FileSystem *fs,eint8* dirname);
```

Arguments

Objects passed to `mkdir` :

- `fs` : pointer to the FileSystem object
- `dir` : pointer to the path + name of the new directory

Return value

Returns 0 if no errors are detected.

Returns non-zero if an error is detected:

- Returns -1 if the directory already exists.
- Returns -2 if the path is incorrect (parent directory does not exists).
- Returns -3 if no free space is available to create the directory.

Example

```
1    #include "efs.h"
2
3    void main(void)
4    {
5        EmbeddedFileSystem efs1;
6
7        /* Initialize efs1 */
8        DBG((TXT(" Will_init_efs1_now.\n")));
9        if (efs_init(&efs1,"/dev/sda")!=0){
10            DBG((TXT(" Could_not_initialize_filesystem_(err_\\%d).\n"),ret));
11            exit(-1);
12        }
13        DBG((TXT(" Filesystem_correctly_initialized.\n")));
14
15        /* Create new directories */
16        if (mkdir(&efs.myFs,"dir1")==0){
17            mkdir(&efs.myFs,"dir1/subdir1");
```

```
18         mkdir(&efs.myFs,"dir1/subdir2");
19         mkdir(&efs.myFs,"dir1/subdir3");
20     }
21
22     /* Close filesystem */
23     fs_umount(&efs.myFs);
24 }
```

5.8 ls_openDir (0.2)

Purpose

This function opens a directory for viewing, allowing you to iterate through it's contents.

Prototype

```
esint8 ls_openDir(DirList *dlist,FileSystem *fs,eint8* dirname);
```

Arguments

Objects passed to `ls_openDir` :

- `dlist` : pointer to a `DirList` object
- `fs` : pointer to the `FileSystem` object
- `dirname` : C string containing the directory path

Return value

This function will return 0 when it has opened the directory, and -1 on error.

Example

```
1 #include "efs.h"
2 #include "ls.h"
3
4 void main(void)
5 {
6     EmbeddedFileSystem efs1;
7     DirList list;
8
9     /* Initialize efs1 */
10    if(efs_init(&efs1,"/dev/sda")!=0){
11        DBG((TXT(" Could not initialize filesystem \n( err \n%d) \n"),ret));
12        exit(-1);
13    }
14
15    /* Open the directory */
16    ls_openDir(list,&(efs1.myFs),"/usr/bin/");
17
18    /* Correctly close the filesystem */
19    fs_umount(&efs1.myFs);
20 }
```

Please note that it is not required to close this object, if you wish to switch to another directory you can just call `ls_openDir` on the object again.

5.9 ls_getNext (0.2)

Purpose

This function fetches the next valid file in the current directory and copies all relevant information to `dirlist->currentEntry`.

Prototype

```
esint8 ls_getNext(DirList *dlist);
```

Arguments

Objects passed to `ls_getNext`:

- `dlist`: pointer to a `DirList` object

Return value

This function will return 0 when it has found a next file in the directory, and was successful in copying it to `dirlist->currentEntry`. It will return -1 when there are no more files in the directory.

Example

To browse through a directory you should first open it with `ls_openDir` and then you can call `ls_getNext` in a loop to iterate through the files. Please note that they are unsorted.

```
1 #include "efs.h"
2 #include "ls.h"
3
4 void main(void)
5 {
6     EmbeddedFileSystem efs1;
7     DirList list;
8
9     /* Initialize efs1 */
10    if(efs_init(&efs1, "/dev/sda")!=0){
11        DBG((TXT(" Could not initialize filesystem (err %d).\n"), ret));
12        exit(-1);
13    }
14
15    /* Open the directory */
16    ls_openDir(list, &(efs1.myFs), "/usr/bin/");
17
18    /* Print a list of all files and their filesize */
19    while(ls_getNext(list)==0){
20        DBG((TXT("%s (%li bytes)\n"),
```

```

21         list->currentEntry.FileName,
22         list->currentEntry.FileSize));
23     }
24
25     /* Correctly close the filesystem */
26     fs_umount(&efs.myFs);
27 }

```

Please note that it is not required to close this object, if you wish to switch to another directory you can just call `ls_openDir` on the object again.

6 Developer notes

6.1 Integer types (0.2)

Standard C data types have the annoying tendency to have different sizes on different compilers and platforms. Therefore we have created 9 new types that are used everywhere throughout the library. When you implement your platform you should check if any of the existing one matches your hardware, or create a new one.

Here's an overview:

Type	Size	Signedness
eint8	1 byte	default to platform
esint8	1 byte	signed
euint8	1 byte	unsigned
eint16	2 bytes	default to platform
esint16	2 bytes	signed
euint16	2 bytes	unsigned
eint32	4 bytes	default to platform
esint32	4 bytes	signed
euint32	4 bytes	unsigned

You will find the relevant code in the file `types.h` in the directory `inc/`.

6.2 Debugging (0.2)

Since debugging on every device is completely different, a `DBG` macro is implemented. On Linux for example, this macro will print the string given to the screen (using `printf`). On AVR, it will send debug strings through the UART. For compatibility with other devices, it is necessary that you always use the `DBG`-macro instead of a device-specific debugging commands.

Because AVR-GCC puts strings in sram memory by default, every string should be surrounded by the `TXT`-macro. On AVR, this macro will put the string in program memory (flash), on any other device, this macro will be ignored.

Example of a debug string:

```
DBG((TXT("This is test nr %d of %d.\n"),id,total));
```

6.2.1 Debugging on Linux

On linux, debugging strings are sent to stdout using `printf`.

To enable debugging, set `DEBUG` in `config.h`.

6.2.2 Debugging on AVR

On AVR, debugging strings are sent through the UART and can be read using a terminal like minicom (linux) or hyperterminal (windows). Standard, the first UART is used, but this can be changed in `debug.c` to the second UART.

To enable debugging:

- Set `DEBUG` in `config.h`
- Set `CLK` to the clock speed of your AVR in `config.h`
- Set `BAUDRATE` to the baudrate you want in `config.h`
- Initialize debugging in your program by calling `debug_init()`

Remark: when you use the serial port in your main program, make sure you use a different UART than the one efsl is using when sending debug string.

6.2.3 Debugging on DSP

On DSP, debugging strings are sent to Code Composer using the `printf` function.

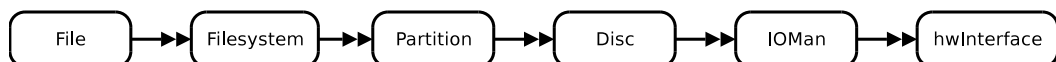
To enable debugging, set `DEBUG` in `config.h`.

Remark: this will only work when using a DSK-kit.

6.3 Adding support for a new endpoint (0.2)

This section will describe step by step how to write an hardware endpoint. You will be required to write your own endpoint in case none of the existing endpoints matches your hardware.

First let's have a look at how EFSL is structured internally.



As you can see we have created a linear object model that is quite simple. The file and filesystem object deal with handling the filesystem specific stuff. Below that we find the Partition object that is responsible for translating partition relative addressing into disc-based LBA addressing.

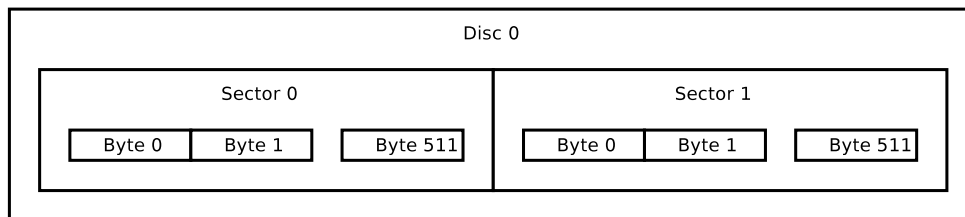
The Disc object holds the partition table, and has a direct link to a cache manager, IOMan. In IOMan, all requests for disc sectors come together. IOMan will perform checks to see if sectors have to be read from disc (or from memory), or written back to disc. In the latter case (reading or writing to disc), a request is made to the hardware layer.

The hardware interface has 3 responsibilities :

- Initialize the hardware

- Read sectors from disc
- Write sectors to disc

All requests are *sector*based, a sector is a 512 byte piece from the disc, that is aligned to a 512 byte boundary.



In this example we will create a new endpoint that will add support for data over pigeon carrier for the EFSL. Initializing the hardware will require feeding the pigeon and telling it where the data is. Reading/Writing will entail giving the bird the sector and letting it fly.

Perform the following steps:

1. Choose a name for your endpoint
You will need this name to create the required defines in the source code. For our example I've chosen the name `PIGEON_CARRIER`. For consistency the final name is then `HW_ENDPOINT_PIGEON_CARRIER`.
2. Verify the sizes of integers
Open `inc/types.h` and create a new entry for pigeon carriers. Perhaps one of the existing sets is identical to yours and you can copy-paste it.
3. Add your endpoint to `interface.h`
Locate the file `interface.h` located in the directory `inc/`. Add a pigeon entry (located above the `#else ... NO INTERFACE DEFINED`)


```

1 #if defined(HW_ENDPOINT_0)
2     #include "interfaces/0.h"
3 #elif defined(HW_ENDPOINT_1)
4     #include "interfaces/1.h"
5 #elif defined(HW_ENDPOINT_PIGEON_CARRIER)
6     #include "interfaces/pigeon.h"
7 #else
8     #error "NO_INTERFACE_DEFINED_-_see_interface.h"
9 #endif
      
```
4. Select your endpoint in `conf/config.h`
5. Create your sourcefiles
Create a header file in `inc/` and a sourcefile in `src/interfaces/`. In this example I'm using `pigeon.h` and `pigeon.c`.
6. Add your object file to the Makefile Take the Makefile that works best on your platform (they should all work with GNU/Make), or create a new

one, using the existing one's as a template. Make sure to include your new pigeon object to the library. If you have an 'ar' like utility you can create a static library, else you may have to create a new project containing all required source files.

The basic framework is now complete, now all that's left to do is to write the code that will perform the actual flying work.

6.3.1 hwInterface

This structure represents the underlying hardware. There are some field that are required to be present (because EFSL uses them), but you may put in as much or a little as your driver requires to access the hardware.

As always in embedded design it is recommended to keep this structure as small as possible.

Example:

```

1 struct hwInterface{
2     /* Field created for THIS hardware */
3     Pigeon pigeon;
4
5     /* Obligatory fields */
6     euInt32 sectorCount;
7 };
8 typedef struct hwInterface hwInterface;
```

6.3.2 if_initInterface

This function will be called one time, when the hardware object is initialized by `efs_init()`. This code should bring the hardware in a ready to use state.

The function's prototype is

```
esInt16 if_initInterface(hwInterface *hw, euInt8* opts);
```

Optionally but recommended you should fill in the `hw->sectorCount` field with the number of sectors. This field is used to validate sector requests.

An example of a `initInterface` function :

```

1 esInt16 if_initInterface(hwInterface *hw, euInt8* opts)
2 {
3     /* Parse options */
4     parse_options(opts); /* Your application may not need options */
5
6     /* Check hardware state */
7     if (!alive(hw->pigeon)){
8         //printf("Pigeon died! :-(\n");
9         return(DEAD_PIGEON); /* #define DEAD_PIGEON -1 */
10    }
11
12    /* Initialize hardware */
```

```

13     feed(hw->pigeon);
14     pet (hw->pigeon);
15
16     /* Get sectors count */
17     hw->numSectors = ask_pigeon_num_sectors(hw->pigeon);
18
19     return(0);
20 }

```

6.3.3 if_readBuf

This function is responsible to read a sector from the disc and store it in a user supplied buffer. You will receive the hardware object, an address and a pointer to memory for storing the buffer.

Please be very careful to respect the boundaries of the buffers, since it will usually be IOMan calling this function, and if you have a buffer overflow you might corrupt the cache of the the next buffer, which in turn may produce extremely rare and impossible to retrace behavior.

The function prototype is:

```
esint16 if_readBuf(hwInterface *hw,euint32 address, euint8* buf);
```

The address is an LBA address, relative to the beginning of the disc. Should you be accessing an old hard disc, or a device which uses some other form of addressing you will have to recalculate the address to your own addressing scheme. Please note that there is no support for sectors that are not 512 bytes large.

```

1 esint8 if_readBuf(hwInterface* hw,euint32 address,euint8* buf)
2 {
3     Message new_message;
4
5     new_message.address = address;
6     new_message.command = READ;
7
8     pigeon_send(hw->pigeon,new_message); /* Launches the pigeon */
9     while(!pigeon_returned(hw->pigeon)); /* Wait until the bird is back */
10    memcpy(new_message.data,buf,512); /* Copy buffer */
11    return(0);
12 }

```

6.3.4 if_writeBuf

The function `if_writeBuf` works exactly the same as it's reading variant.

6.4 I/O Manager (0.2)

The IOManager that is the second lowest layer of the embedded filesystems library is responsible for coordinating disk input and output, as well as managing

a caching system. This documentation describes the second implementation of IOMan, which includes features such as :

- Delayed write
- Buffer reference statistics
- Buffer exportable to users
- Support for cached direct I/O as well as indirect I/O
- Can allocate memory itself (on the stack), or you can do it yourself (heap)

6.4.1 General operation

Because of the limited memory nature of most embedded devices for which this library is intended several design decisions were made to minimize memory usage. Some of these required that some concessions be made. One of them is that there is no memory protection, since most devices don't have the memory to support this, or lack the ability to protect memory.

When IOMan receives a request for a sector, it will make sure it has the sector in it's own memory cache and then give the caller a `eu_int8*` pointer to that cache. The user is then free to do operations on that memory, and when it is done it should tell IOMan so. Several things can go wrong with this: you can request a sector for reading, and then write in the cache, thereby corrupting it. Or you can request a sector, but never release it (sort of a memory leak), which may result in very bad performance, and a deadlocked I/O manager.

But, taking into account that very little memory is required for operation, if you follow the I/O man rules, you will get a pretty clever caching object that will make writing new filesystems a simple job.

6.4.2 Cache decisions

Whenever ioman receives a request to fetch a sector, be it read or write, it will have to make sure it has, or can get the sector you want. It follows a certain path to do this.

1. First of all it will scan it's cache range to see if it already has the sector. If it is found, and it was a write request, the cache is marked writable. Usage and reference get incremented and a pointer is then returned to the requester. If the buffer cannot be found, ioman proceeds to step 2.
2. When an item is not in cache, it has to be fetched from the disc, the best place to store it is in memory that does not contain anything useful yet. Ioman will search for a place that is currently not occupied by anything. If it is found, the sector will be placed on that spot and a pointer returned. Else, ioman proceeds to step 3.

3. Since there is no other choice than to overwrite an already existing cache, ioman will try to find one that is the least interesting. First it will search for caches that are marked not writable, and have no users. Ioman will then select the one that has the least references. If there are none, it will search for caches that don't have users and are writable. Once again the one with the least references is returned. Since it is writable ioman will flush it to disc first. After that the requested sector is put there and a pointer returned. If it cannot find any caches that have no users it will go to step 4.
4. Since every cache spot is in use ioman will have to select one for overallocation. Since this selection depends on many factors and is rather complex, a points system is used. The algorithm considers every cache place and allocated a certain number of points to it, lower means that it is a better candidate for overallocation. Fifty percent of the points goes to the cache being marked writable, since having to write a sector is expensive. Another 35 percent goes to how many overallocations have already been done on that spot. It doesn't make sense to always overalloc the same buffer, it is better to spread this. The remaining 15 percent is determined by the number of references to the sector.

After a function has selected the best candidate, ioman will overwrite that spot with the new sector. It will also push the status and sectornumber onto that cache's retrieval stack, so that when the sector is released, the older sector can be retrieved. If this fails go to step 5.
5. When ioman gets here it will return a (nil) pointer and flag an error.

6.4.3 Functions

I/O Manager Functions	
<code>ioman_init</code>	<code>esint8 (IOManager *ioman, hwInterface *iface, euint8* bufferarea)</code>
This function is called to initialize the internal state of the I/O manager. It should be the first function you call on an ioman object. Failure to do so will result in undefined behavior. The function clears all internal variables to a default safe state, and sets up it's memory region. There are two possibilities, if you supply a 0 pointer then a function will be called that contains a static variable with a size of 512 * IOMAN_NUMBUFFERS , else, it will be assumed that you allocated that memory yourself and the pointer you provided will be used.	
<code>ioman_reset</code>	<code>void (IOManager *ioman)</code>
This function is called from the initialization function, it does the actual reset of all variables.	
<code>ioman_pop</code>	<code>esint8 (IOManager *ioman,euint16 bufplace)</code>
This function fetches settings (sector number, usage and status register) from stack <code>bufplace</code> and puts it back on the main registers. It will return 0 on successful pop, and -1 on error, or when there are no elements to pop.	
<code>ioman_push</code>	<code>esint8 (IOManager *ioman,euint16 bufplace)</code>

I/O Manager Functions (continued)	
<p>This function pushes the settings of cache <code>bufplace</code> onto that cache's stack. It does not destroy the data in the main registers. It will return 0 for a successful push, and -1 on error, or when there is no more space to push a new element.</p>	
<code>ioman_readSector</code>	<code>esint8 (IOManager *ioman,euint32 address,euint8* buf)</code>
<p>This function does the actual reading from the hardware, it is the one and only function that calls <code>if_readBuf()</code> , here a retry on failure policy could be implemented. This function will correctly stream errors upwards. All calls made to this function in the iomanager are checked for their return value, so errors propagate correctly upwards.</p> <p>The address it receives is relative to the beginning of the disc, no assumptions about <code>buf</code> may be made, it can be withing ioman's cache memory range, but it could also be a buffer from userspace.</p> <p>The function will return 0 on success and -1 on failure.</p>	
<code>ioman_writeSector</code>	<code>esint8 (IOManager *ioman, euint32 address, euint8* buf)</code>
<p>This function does the actual writing to the hardware, it is the one and only function that calls <code>if_writeBuf()</code> , here a retry on failure policy could be implemented. This function will correctly stream errors upwards. All calls made to this function in the iomanager are checked for their return value, so errors propagate correctly upwards.</p> <p>The address it receives is relative to the beginning of the disc, no assumptions about <code>buf</code> may be made, it can be withing ioman's cache memory range, but it could also be a buffer from userspace.</p> <p>The function will return 0 on success and -1 on failure.</p>	
<code>ioman_getSector</code>	<code>euint8* (IOManager *ioman,euint32 address, euint8 mode)</code>

I/O Manager Functions (continued)	
This function is the one that is called most from the higher library routines. It is the function that will present you with a pointer to memory containing sector number <code>address</code> . There are several modes that you can select or combine.	
IOM_MODE_READONLY	This attribute says to ioman that it needs a buffer only for reading. This does not mean that you are allowed to write to it, doing so results in undefined behavior. You cannot combine this option with the IOM_MODE_READWRITE option.
IOM_MODE_READWRITE	This attribute says to ioman that it would like not only to read from but also to write to that buffer. When you release the sector your changes will be written to disc. This may not happen immediately though, if you want to force it take a look at the <code>ioman_flushRange()</code> function. This option cannot be combined with the IOM_MODE_READONLY option.
IOM_MODE_EXP_REQ	This option tell the iomanager that the request is exceptional, for example that the request is unlikely to happen again. The library adds this flags to the options when requesting the bootrecord, to prevent it from getting a high rating, which should prevent it from being removed from the cache.
These options can be combined by ORing them together.	
<code>ioman_releaseSector</code>	<code>esint8 (IOManager *ioman,euint8* buf)</code>
This function tells ioman that you are done with one of the cache elements and that it can do it's bidding with it. Forgetting to call this function may result in deadlocked iomanagers.	
<code>ioman_directSectorRead</code>	<code>esint8 (IOManager *ioman,euint32 address,euint8* buf)</code>
This is a variant of the normal getsector. Sometimes you need a sector from the disc, but all you want to do with it is export it directly to userbuffers. It would be foolish to force a caching of that sector if there is external space available for it. This function will fetch sector <code>address</code> from disc and place it in the memory pointed to by <code>buf</code> . Should there be a free spot available the sector will be cached there, so that it may be used in the future. If the sector was available from cache in the first place, it will simply be <code>memCpy()</code> 'd from the cache to the userspace buffer.	
<code>ioman_directSectorWrite</code>	<code>esint8 (IOManager *ioman,euint32 address,euint8* buf)</code>
This function is based on the same philosophy as <code>ioman_directSectorRead()</code> , however, contrary to what the name may lead to believe it also passes through a caching layer. If there is an unused spot (or the sector is in cache), the userbuffer will be copied to that spot and will remain there until the space is needed or a flush is forced.	
<code>ioman_flushRange</code>	<code>esint8 (IOManager *ioman,euint32 address_low, euint32 address_high)</code>

I/O Manager Functions (continued)	
This function is used to ask ioman to flush all sectors to disc that are in a specific range. For example you might want to flush a specific range of your filesystem without needlessly disturb other parts. The range is <code>address_low <= n => address_high</code> . Off course only sectors that are marked as writable are flushed to disc.	
<code>ioman_flushAll</code>	<code>esint8 (IOManager *ioman)</code>
This function will cause ioman to flush out all cache units that are marked writable. If they do not have any users, they will lose their writable mark.	

6.5 C library for EFSL (0.2)

This section of the manual describes the minimalistic C library functions that were created for EFSL. Since EFSL was designed for ultimate portability, no assumptions about the workings or even the presence of a C library could be made. Fortunately only very few functions had to be created that mimicked the operations of well known C library functions.

PLibC Functions	
<code>strMatch</code>	<code>euint16 strMatch(eint8* bufa, eint8*bufb,euint32 n)</code>
This function compares the strings <code>bufa</code> and <code>bufb</code> for <code>n</code> bytes. It will return the number of bytes in that section that does not match. So if you want to compare two strings the return value should be 0, for the strings to match over the entire <code>n</code> area.	
<code>memCpy</code>	<code>void memCpy(void* psrc, void* pdest, euint32 size)</code>
This function will copy the contents at location <code>psrc</code> to location <code>pdest</code> over a range of <code>size</code> bytes.	
<code>memClr</code>	<code>void memClr(void *pdest,euint32 size)</code>
This function will set the memory at <code>pdest</code> to value <code>0x00</code> for a range of <code>size</code> bytes.	
<code>memSet</code>	<code>void memSet(void *pdest,euint32 size,euint8 data)</code>
This function will set the memory at <code>pdest</code> to value <code>data</code> for a range of <code>size</code> bytes.	

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