

Funkey
project

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Michel Stempin

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File: FunKey.kicad_pcb

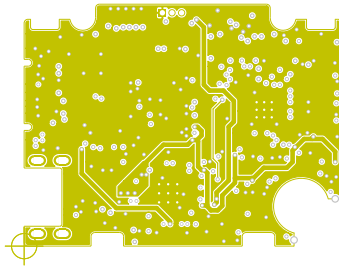
Title: *FunKey*

Size: User	Date: 2020-11-27
KiCad E.D.A.	kicad 5.1.5+dfsg1-2build2



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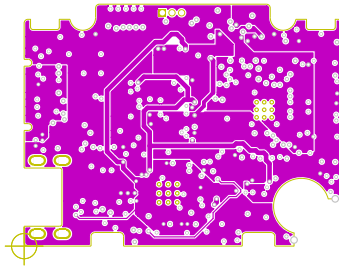
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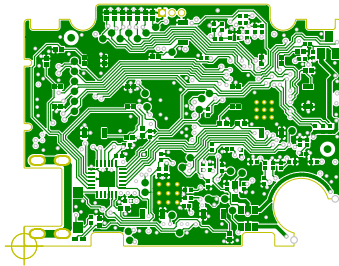


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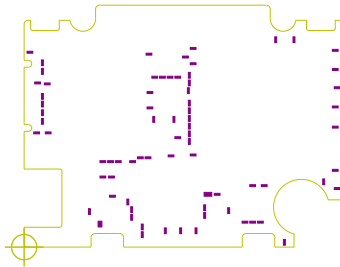


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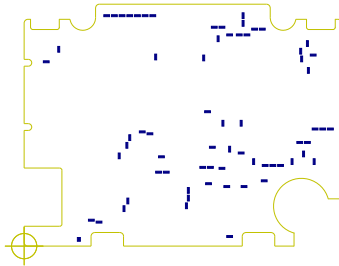


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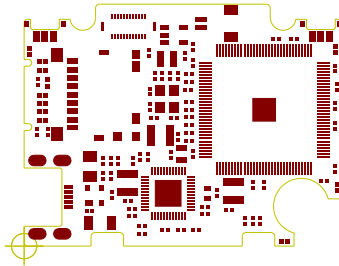
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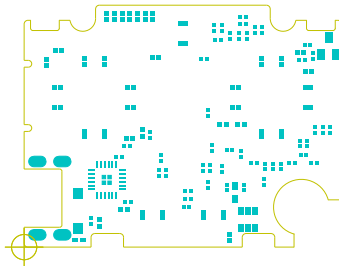
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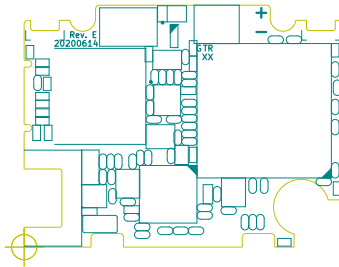
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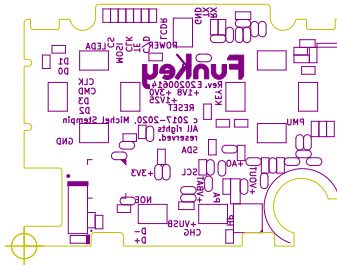
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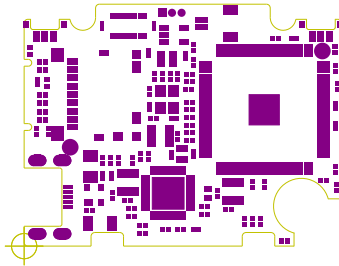
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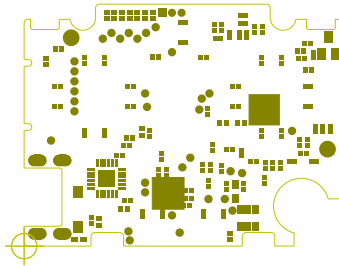


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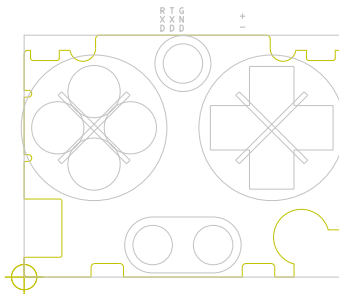
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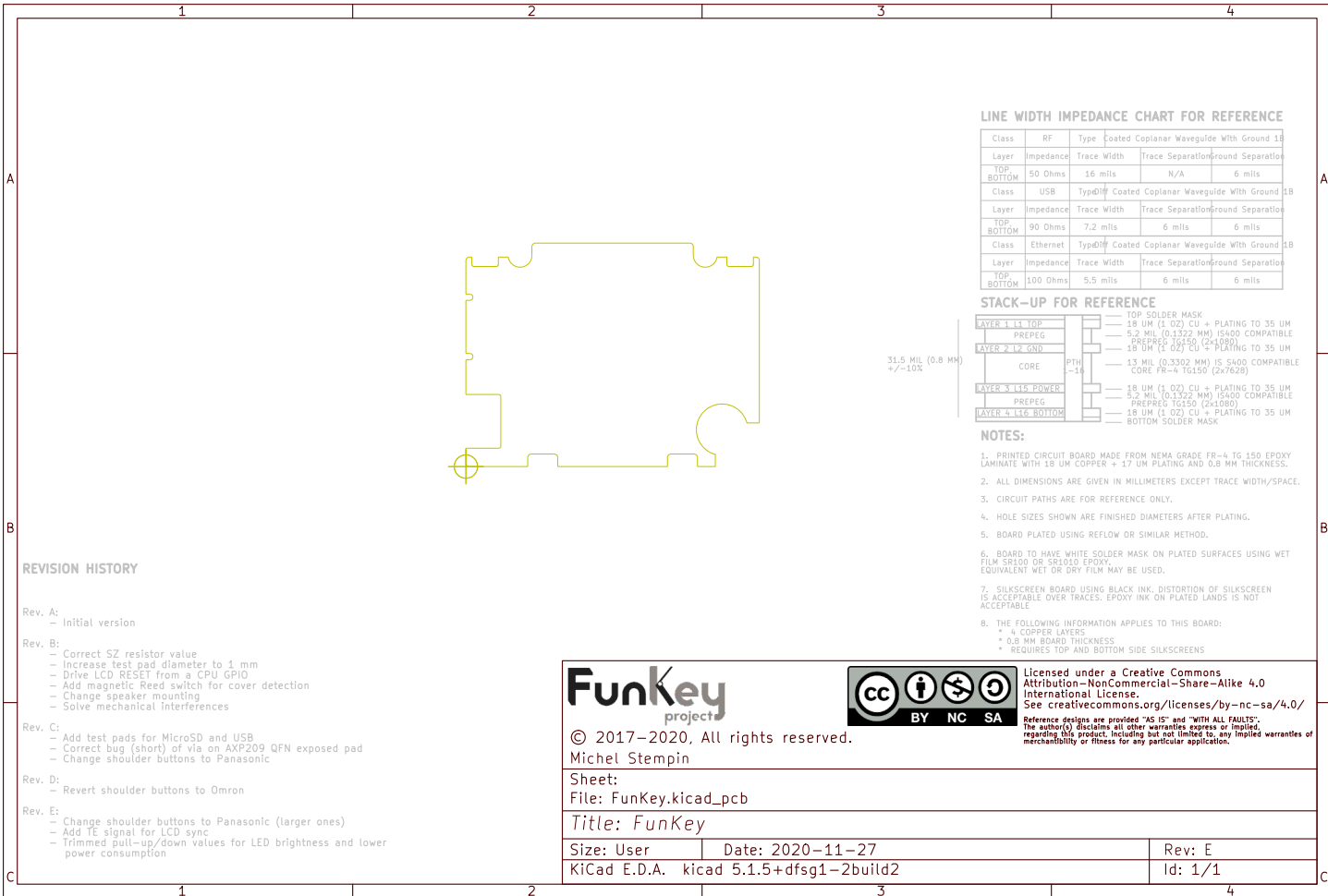
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LINE WIDTH IMPEDANCE CHART FOR REFERENCE

Class	RF	Type	Coated Coplanar Waveguide With Ground 19		
Layer	Impedance	Trace Width	Trace Separation	Ground Separation	
TOP	50 Ohms	16 mils	N/A	6 mils	
BOTTOM					
Class	USB	Type#19	Coated Coplanar Waveguide With Ground 19		
Layer	Impedance	Trace Width	Trace Separation	Ground Separation	
TOP	90 Ohms	7.2 mils	6 mils	6 mils	
BOTTOM					
Class	Ethernet	Type#19	Coated Coplanar Waveguide With Ground 19		
Layer	Impedance	Trace Width	Trace Separation	Ground Separation	
TOP	100 Ohms	5.5 mils	6 mils	6 mils	
BOTTOM					

STACK-UP FOR REFERENCE

LAYER 1 (4 TOP)	PREPREG	PTH	TOP SOLDER MASK
LAYER 2 (2 GRD)	CORE	-16	18 UM (1.0Z) CU + PLATING TO 35 UM
LAYER 3 (16 POWER)	PREPREG		5.2 MIL (0.1322 MM) 15400 COMPATIBLE PREPREG TG150 (2x1090)
LAYER 4 (16 BOTTOM)			18 UM (1.0Z) CU + PLATING TO 35 UM
			5.2 MIL (0.1322 MM) 15400 COMPATIBLE PREPREG TG150 (2x1090)
			18 UM (1.0Z) CU + PLATING TO 35 UM
			BOTTOM SOLDER MASK

NOTES:

- PRINTED CIRCUIT BOARD MADE FROM NEMA GRADE FR-4 TG 150 EPOXY LAMINATE WITH 18 UM COPPER + 17 UM PLATING AND 0.8 MM THICKNESS.
- ALL DIMENSIONS ARE GIVEN IN MILLIMETERS EXCEPT TRACE WIDTH/SPACE.
- CIRCUIT PATHS ARE FOR REFERENCE ONLY.
- HOLE SIZES SHOWN ARE FINISHED DIAMETERS AFTER PLATING.
- BOARD PLATED USING REFLOW OR SIMILAR METHOD.
- BOARD TO HAVE WHITE SOLDER MASK ON PLATED SURFACES USING WET FILM S1810 OR S1810D EPOXY. EQUIVALENT WET OR DRY FILM MAY BE USED.
- SILKSCREEN BOARD USING BLACK INK. DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. EPOXY INK ON PLATED LANDS IS NOT ACCEPTABLE.
- THE FOLLOWING INFORMATION APPLIES TO THIS BOARD:
 - * 4 COPPER LAYERS
 - * 0.8 MM BOARD THICKNESS
 - * REQUIRES TOP AND BOTTOM SIDE SILKSCREENS

REVISION HISTORY

- Rev. A:
- Initial version
- Rev. B:
- Correct SZ resistor value
- Increase test pad diameter to 1 mm
- Drive LCD RESET from a CPU GPIO
- Add magnetic Reed switch for cover detection
- Change speaker mounting
- Solve mechanical interferences
- Rev. C:
- Add test pads for MicroSD and USB
- Correct bug (short) of via on AXP209 QFN exposed pad
- Change shoulder buttons to Panasonic
- Rev. D:
- Revert shoulder buttons to Omron
- Rev. E:
- Change shoulder buttons to Panasonic (larger ones)
- Add TE signal for LCD sync
- Trimmed pull-up/down values for LED brightness and lower power consumption



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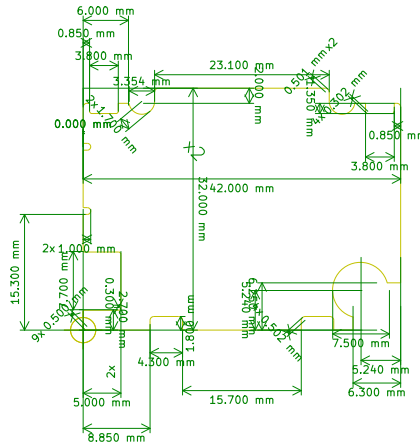
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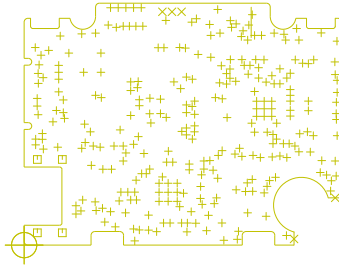
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DRILL CHART: TOP TO BOTTOM

Sym	N°	Mils	MM	Qty	Plated
+	1	12	0.30	287	YES
X	2	28	0.71	5	YES
□	3	31	0.80	4	YES



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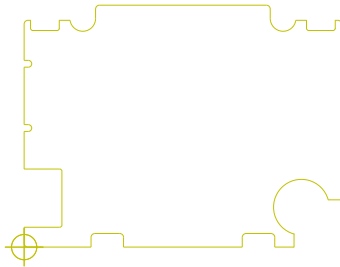
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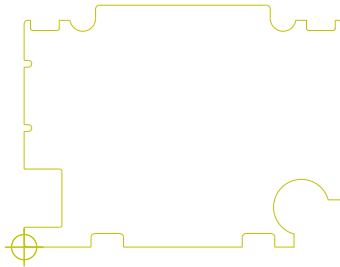
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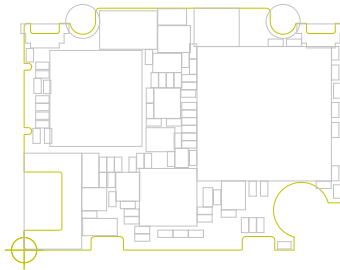
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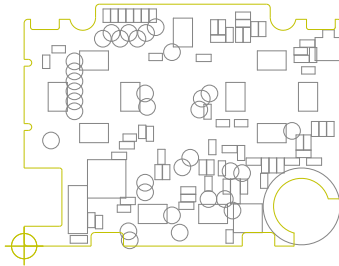
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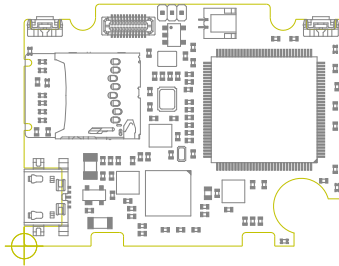
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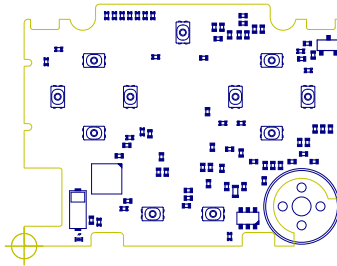


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